

09-751-795

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,608,761 B1
DATED : August 19, 2003
INVENTOR(S) : Robert D. Wachel

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4.

Line 58, "a hoard; and" should be -- a board; and --

Column 5.

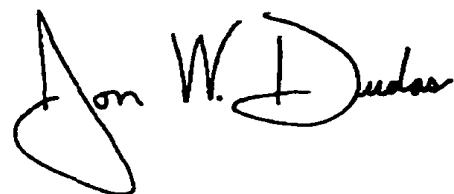
Line 11, "a processor mourned" should be -- a processor mounted --

Column 6.

Line 21 "PCI bridge hoard" should be -- PCI bridge board; --

Signed and Sealed this

Sixteenth Day of November, 2004



JON W. DUDAS
Director of the United States Patent and Trademark Office